MITL Specification Debugging for Monitoring of Cyber-Physical Systems

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Trust? : Sampling of automotive recalls (~2011-12) due to software errors ...

•	"A software erro 4th gear when c decreased engir	No downshifting from 5 th to 4 th	fting from 5th to his may result in "
•	the software th ECU monitors continued the software pice	at "allows the ECU to establish a 'handshake' with the engine Rough idling or stalling due to complicated	is in error. The It of tolerance, code. As the ECU
	tries to find an c situation ensues."	adaptive ECU	ugh idle or stalling
•	to up is possik opposite	tric motor to rotate in the direction opposite to tl selected by the transmission	nat stances, it
•	If the fault occu which would me or pressing the	Cruise control does not disengage unless turning off the ignition	while driving - steering. Braking
•		Many more	





Trust? : Sampling of automotive recalls (~2011-12) due to software errors ...

•	"A software e 4th gear whe decreased en	Under C(x,p) conditions the system should always switch from 5 th to 4 th gear.	fting from 5th to is may result in "
•	the softwar ECU monitors the software tries to find a	re that "allows the FCU to establish a 'handshake' with the engine p p The engine should never stall while idle. n	is in error. The ut of tolerance, code. As the ECU ugh idle or stalling
	situation ensu	ies."	
•	to up is possik opposite	The electric motor should always rotate in the directi selected by the transmission.	on stances, it
•	If the fault which woul or pressing	The cruise control should always disengage when the "turn off" button is pressed.	hile driving - eering. Braking
•			





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Formal Specification

- Natural Language is not appropriate for verification
 - Ambiguous, inaccurate, inconsistent?
- Alternative: use mathematical logic
 - Elicitation process: Challenging & Error prone
- Validate the specification
 - Verification with wrong specification is useless
- Before verification we need to analyze the specification





Previous Work: ViSpec

- ViSpec helps transforming Pre-specified templates in NL
- Easy to use

University

 No need for MITL background



Alw	ays: rpm	Template ID: 0		
	- 5000	always 👻		
	3000	rpm		
	- 2000		Above	R
×	- 1000	Always, from 0 to 30 the signal will be true when	Below	3000

- B. Hoxha and H. Bach and H. Abbas and A. Dokhanchi and Y. Kobayashi and G. Fainekos, **Towards Formal Specification Visualization for Testing and Monitoring of Cyber-Physical Systems,** DIFTS 2014
- B. Hoxha and N. Mavridis and Georgios Fainekos, VISPEC: A graphical tool for easy elicitation of MTL requirements, IROS 2015
 ARIZONA STATE



ViSpec – Specification Classes

Safety:

 $\Box_I \phi$

 $\lambda_I \phi$

Reachability:

Stabilization: $\Diamond_I \Box_I \phi$

Implication:

$$\phi \rightarrow \psi$$

Reactive Response: $\Box_{I}(\phi \to M_{I}\psi)$

Conjunction: $\phi \wedge \psi$

Recurrence:

 $\Box_I \diamondsuit_I \phi$

Non-strict Sequencing: $N_I(\phi \wedge M_I\psi)$

 $M \in \{\Box,\diamondsuit\}, N \in \{\Box,\diamondsuit\}$





Motivating Example: On-Line Survey

We asked:

"At some time in the first 30 seconds, the vehicle speed (v) will go over 100 and stay above 100 for 20 seconds"

Response:

$$\varphi = \diamondsuit_{[0,30]} (v > 100) \Rightarrow \Box_{[0,20]} (v > 100)$$

 φ is a tautology

- $(v > 100) = \bot$ any time in [0,30] $((v > 100) \Longrightarrow \square_{[0,20]}(v > 100)) = \top$
- (v > 100) = T all the time in [0,30]

$$\Box_{[0,20]} (v > 100) = \mathsf{T} \mathsf{between} [0,10]$$
$$((v > 100) \textcircled{\Rightarrow} \Box_{[0,20]} (v > 100)) = \mathsf{T} \mathsf{between} [0,10]$$

• B. Hoxha and N. Mavridis and Georgios Fainekos, VISPEC: A graphical tool for easy elicitation of MTL requirements, IROS 2015

Problem Formulation

Problem 1 (System Independent MITL Analysis):

Given an MITL formula φ , find whether φ has any of the following logical issues:

- Validity: the specification is unsatisfiable or a tautology.
- Redundancy: the formula has redundant conjuncts.
- Vacuity: some subformulas do not contribute to the satisfiability of the formula.

Problem 2 (System Dependent Vacuity Checking): Given an MITL formula ϕ , and signal μ , check whether μ satisfies the antecedent failure mutation of ϕ .

- 1. A. Dokhanchi, B. Hoxha, and G. Fainekos, *Metric interval temporal logic specification elicitation and debugging*. MEMOCODE 2015, Austin, TX, USA
- 2. Extension of 1 under review





Contributions

- We present a specification debugging algorithm for a fragment of Metric Interval Temporal Logic (MITL) specifications and, consequently for Signal Temporal Logic (STL).
 - We extend Linear Temporal Logic (LTL) vacuity detection algorithms to real-time specifications in MITL.
- We provide a signal vacuity detection algorithm to indicate to the testing team the signals that vacuously satisfy the specification.
- We present experimental results on specifications that typically appear in CPS specifications.





Overview

- Motivation
- Preliminaries
- System Independent MITL Analysis
- System Dependent Vacuity Checking
- Experiments
- Conclusion & Future Research





Metric Interval Temporal Logic: Semantic Intuition

 $\phi ::= \top | p | \neg \phi | \phi_1 \lor \phi_2 | \Box_I \phi | \diamondsuit_I \phi | \phi_1 U_I \phi_2$



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Subset of MITL

- Bounded-MITL(\diamondsuit , \Box) with only Always & Eventually operator
- Negation Normal Form
- Syntax:

$$\phi ::= \top \mid \bot \mid p \mid \neg p \mid \phi_1 \lor \phi_2 \mid \phi_1 \land \phi_2 \mid \Box_I \phi \mid \diamondsuit_I \phi$$

• No Until, Release, Next operator is used







Notice example is MITL if we replace the predicate with a proposition: $a \equiv (x(t) \ge x_0)$

ah.



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Debugging MITL Specification

Specification Elicitation Framework



Revision Necessary

3-Levels of Specification Debugging







Validity Issues Detection

Checking whether φ is unsatisfiable or a tautology A valid formula is one where φ and $\neg \varphi$ are satisfiable

We asked:

"At some time in the first 30 seconds, the vehicle speed (v) will go over 100 and stay above 100 for 20 seconds"

Response:

Φ

$$\varphi = \diamondsuit_{[0,30]}(\ (v>100) \Rightarrow \Box_{[0,20]}(v>100)\)$$
 is a tautology





Redundancy Issues Detection

Conjunctive formula:
$$\Phi = \bigwedge_{j=1}^k \varphi_j$$

Removing conjunct:

$$\bigwedge_{j=1}^{i-1} \varphi_j \wedge \bigwedge_{j=i+1}^k \varphi_j \equiv \Phi \backslash \varphi_i$$

If $\exists \varphi_i$

 $\Phi \backslash \varphi_i \vDash \varphi_i$

Then φ_i is redundant

Example
$$\varphi_2 = p \land \Box_{[0,10]} p \longrightarrow \Box_{[0,10]} p \vDash p$$

Algorithm 1: Checks $\Phi \setminus \varphi_i \vDash \varphi_i$ for each conjunct Creates a list of redundant conjuncts

H. Chockler and O. Strichman, Before and after vacuity. Form. Methods Syst. Des., 34(1):37–58, Feb. 2009.
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Redundancy Example

CPS example

$$\varphi = \diamondsuit_{[0,30]}(speed > 100) \land \diamondsuit_{[0,20]}(speed > 100)$$

 $\diamondsuit_{[0,30]}(v > 100)$ is redundant since

$$\Diamond_{[0,20]}(v > 100) \vDash \Diamond_{[0,30]}(v > 100)$$





Vacuity Issues Detection

If sub-formula $\psi \in \varphi$ does not affect the satisfiability of φ , then φ is vacuous

Remove ψ

Vacuous specifications are **equivalent** to their mutant

H. Chockler and O. Strichman, Before and after vacuity. Form. Methods Syst. Des., 34(1):37–58, Feb. 2009.





Mutation of MITL for Vacuity Checking

Mutation with assigning \perp to literal occurrence

$$\varphi = (\neg p \land q) \lor \diamondsuit_{[0,10]} p \lor \Box_{[0,10]} q$$
$$\varphi[\neg p \leftarrow \bot] = (\bot \land q) \lor \diamondsuit_{[0,10]} p \lor \Box_{[0,10]} q$$

4 literal occurrence => 4 mutation

Algorithm 2: Checks $\Phi \models \varphi_i[l \leftarrow \bot]$ for each mutation Creates a list of mutated sub-formulas





Vacuity Theorem

Theorem (MITL Vacuity with respect to Specification): Assume that the specification Φ is a conjunction of MITL formulas. If $\exists \varphi_i \in \Phi$ and $\exists l \in \text{litOccur}(\varphi_i)$, such that $\Phi \models \varphi_i \ [l \leftarrow \bot]$, then Φ satisfies φ_i vacuously ($\Phi \models_v \varphi_i$).

We proved that in $\boldsymbol{\Phi}$

if ∃ a conjunct $φ_i$ & literal occurrence $l \in litOccur(φ_i)$ s.t Φ ⊨ $φ_i[l \leftarrow ⊥]$

then Φ is inherently vacuous

A. Dokhanchi, B. Hoxha, and G. Fainekos, *Metric interval temporal logic specification elicitation and debugging*. MEMOCODE 2015, Austin, TX, USA,





Vacuity Example

$$\label{eq:cps} \begin{split} \mathsf{CPS} \ \mathsf{example} \\ \varphi_{STL} = \diamondsuit_{[0,10]} ((speed > 100) \land \diamondsuit_{[0,10]} (speed > 80)) \end{split}$$

 $\varphi = \diamondsuit_{[0,10]}(a \land \diamondsuit_{[0,10]} b)$ is not vacuous

However...

$$\varphi' = \diamondsuit_{[0,10]}(a \land \diamondsuit_{[0,10]}(a \lor c))$$
 is vacuous

Where a:(speed > 100) and $c:(100 \ge speed > 80)$

$$\varphi' \vDash \diamondsuit_{[0,10]}(a \land \diamondsuit_{[0,10]}(a \lor \bot))$$





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Vacuous Signals

• The MITL specification

 $\varphi = \Box_{[0,5]}((request) \Rightarrow \diamondsuit_{[0,10]}(acknowledge))$

- φ is passed the MITL Specification Debugging Framework
- Any signal μ that does not satisfy request at any point in time will vacuously satisfy φ .
- Signals that do not satisfy the *antecedent* (*precondition*) of the subformula are called vacuous signals.
- Vacuous Signals satisfy antecedent failure mutation of arphi





Antecedent Failure Mutation

- For each implication ($\phi \Rightarrow \psi$), (ϕ) is the precondition (antecedent) of the implication.
- Antecedent Failure Mutation is the assertion that the precondition (φ) never happens.
- Example:

Antecedent Failure of ϕ is $\neg \phi$

• Signals that satisfy $\neg \phi$ are vacuous signals





Vacuity Detection in Testing







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Implementation and Experiments

We used MITL satisfiability solver for this method: $\varphi \models \psi \quad \text{iff} \ (\varphi \Longrightarrow \psi) \equiv T \quad \text{iff} \ (\neg \varphi \lor \psi) \equiv T \quad \text{iff} \ (\varphi \land \neg \psi) \equiv \bot$ We detected all the issues with MITL SAT solver



M. Bersani and M. Rossi and P. San Pietro, A tool for deciding the satisfiability of continuous-time metric temporal logic. Acta Informatica, pages 1–36, 2015.





ViSpec – Usability Study

Each user received ten tasks:

• To formalize a NL specification in automotive industry through ViSpec



<u>Group I: Non-expert users</u> No experience in working with requirements.

20 subjects from the student community at ASU

Group 2: Expert users

Experienced in working with requirements (not necessarily formal requirements) 10 subjects from the industry in the Phoenix area

B. Hoxha and N. Mavridis and Georgios Fainekos, VISPEC: A graphical tool for easy elicitation of MTL requirements, IROS 2015





Specification Checks

Seven of ten tasks have no detected issue

Example task with erroneous specifications:

Stabilization "At some point in time in the first 30 seconds, vehicle speed will go over 100 and stay above for 20 seconds."

Correct answer:
$$\Diamond_{[0,30]} \Box_{[0,20]} (p_1)$$

p₁ : speed>100

Incorrect Answers:

Specification	Detected Error
$\diamondsuit_{[0,30]}$ (p ₁) $\land \diamondsuit_{[0,20]}$ (p ₁)	$\diamondsuit_{[0,30]}$ (p ₁) is redundant
$\diamondsuit_{[0,30]}(p_1 \Rightarrow \square_{[0,20]}(p_1))$	Tautology





Error in Oscillation Task

"At every point in time in the first 40 seconds, vehicle speed will go over 100 in the next 10 seconds."

Correct answer:

$$\Box_{[0,40]} \diamondsuit_{[0,10]} (p_1)$$

p₁ : speed>100

Incorrect answer (with Redundancy Error):

$$\Box_{[0,40]}(p_1) \land \Box_{[0,40]} \diamondsuit_{[0,20]}(p_1)$$

Issue:

$$\Box_{[0,40]} \diamondsuit_{[0,20]} (p_1)$$
 is redundant





Error in Long Sequence Task

"If, at some point in time in the first 40 seconds, vehicle speed goes over 80 then from that point on, if within the next 20 seconds the engine speed goes over 4000, then, for the next 30 seconds, the vehicle speed should be over 100." $\diamond_{[0,40]}((speed>80) \Rightarrow \diamond_{[0,20]}(rpm>4000 \Rightarrow \Box_{[0,30]}speed>100))$ \downarrow (STL2MITL) $\diamond_{[0,40]((}p_1 \lor p_3) \Rightarrow \diamond_{[0,20]}(p_2 \Rightarrow \Box_{[0,30]}p_1))$ $p_1: speed>100 p_2: rpm>4000 p_3: 100≥speed>80$

Incorrect Answers:

Specification	Detected Error
$(p_{1} \vee p_{3}) \Rightarrow (p_{2} \wedge \square_{[0,30]} p_{1}))$	Vacuous formula
$(p_{[0,40]}(p_1 \vee p_3) \land (p_{[0,40]} \vee p_2 \land (p_{[0,40]} \square_{[0,30]} p_1))$	$\Diamond_{[0,40]}(p_1 \vee p_3)$ is redundant





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Runtime Overhead







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Antecedent Failure Detection

Requirement	Natural Language	MITL Formula
	There should be no transition from	
ϕ_1^{AT}	gear two to gear one and back to	$\Box_{[0,27.5]}((g_2 \land \diamondsuit_{(0,0.04]}g_1) \Rightarrow \Box_{[0,2.5]} \neg g_2)$
_	gear two in less than 2.5 sec.	
	After shifting into gear one, there	
ϕ_2^{AT}	should be no shift from gear one to	$\Box_{[0,27,5]}((\neg g_1 \land \Diamond_{(0,0.04]}g_1) \Rightarrow \Box_{[0,2,5]}g_1)$
_	any other gear within 2.5 sec.	
	If the engine speed (ω) is always less	
ϕ_3^{AT}	than 4500, then the vehicle speed (v)	$\Box_{[0,30]}(\omega \le 4500) \Rightarrow \Box_{[0,10]}(v \le 85)$
	can not exceed 85 in less than 10 sec.	
	Within 10 sec. the vehicle speed is less	
ϕ_3^{AT}	than 80 and from that point on the	$\diamond_{[0,10]}((v \le 80) \Rightarrow \Box_{[0,30]}(\omega \le 4500))$
	speed engine is always less than 4500.	

Requirement	Antecedent Failure	Vacuous Signals / All Signals
ϕ_1^{AT}	$\Box_{[0,27.5]} \neg (g_2 \land \diamond_{(0,0.04]} g_1)$	1989 / 2000
ϕ_2^{AT}	$\Box_{[0,27.5]} \neg (\neg g_1 \land \diamond_{(0,0.04]} g_1)$	1994 / 2000
ϕ_3^{AT}	$ egreen{aligned} egreen{alig$	60 / 214
ϕ_3^{AT}	$\Box_{[0,10]} \neg (v \le 80)$	1996 / 2000

B. Hoxha, H. Abbas and Georgios Fainekos, Benchmarks for Temporal Logic Requirements for Automotive Systems, ARCH 2014





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Conclusions

- We developed a debugging framework for MITL
- We extended the existing LTL vacuity detection algorithms to MITL
- We used utility result to check that specification is common
- We implemented the antecedent failure detection algorithm that can find signal vacuity
- Our tool can improve the users ability to create correct MITL specifications





Future Research

- Integrate MITL analysis into ViSpec
- Finding the Coverage of specification with respect to falsifying signals
- Improving the stochastic search algorithms for falsification of the requirement in CPS with signal vacuity detection.





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